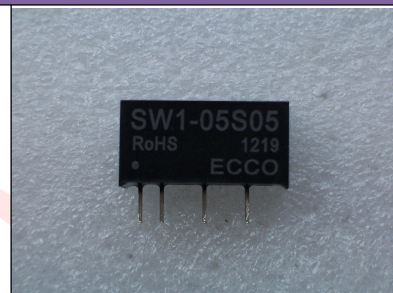


Typical Performance

FEATURES

- Fixed Input, isolation, regulated Output,1W
- Isolation voltage: 1000VDC,1500VDC,3000VDC
- SIP package
- Efficiency :up to 80%
- Working temperature -40℃~+85℃
- MTBF≥35x10⁵Hrs
- Industry standard pinout
- No heat sink required
- No external component required
- In line with RoHS codes
- Line regulation (for Vin change of ±5%): ±0.25%(max)
- Load regulation (10%-100% load) :1%
- Ripple(20MHz Band width) <20mVp-p,noise(20MHz Band width)<100mVp-p
- Temperature drift(100% full load):±0.03%/℃(max)
- Switching Frequency(Full load,nominal input):100Khz(typ)
- Storage Temperature:-55℃~+125℃
- Isolation Resistance:1000MΩ/1min
- Isolation capacitance:130Pf(typ)
- Cooling:Free air convection



SW 1	XX	S/D	XX	S/S3
① ②	③	④	⑤	⑥

- ① Series name
- ② Output watt
- ③ Normal input voltage
- ④ S:Single output
D: Dual output
- ⑤ Output voltage
- ⑥ I/O isolation(S:1KV,no S:1.5KV,S3:3KV)

Product Program

Part #	Input voltage range	Nominal output voltage / output current						Efficiency (%, typ)
		VO1			VO2			
		Voltage (VDC)	Min (mA)	Max (mA)	Voltage (VDC)	Min (mA)	Max (mA)	
SW1-03S3V3	3V(3~3.6VDC)	3.3V	30	303				
SW1-03S05		5V	20	200				
SW1-05S05	5V（4.75~5.25VDC）	5V	20	200				
SW1-05S09		9V	11	110				
SW1-05S12		12V	8	83				
SW1-05S15		15V	6.8	68				
SW1-05S24		24V	4.2	42				
SW1-05D05S		5V	10	100	-5V	10	100	
SW1-05D09		9V	5.5	55	-9V	5.5	55	
SW1-05D12		12V	4	40	-12V	4	40	

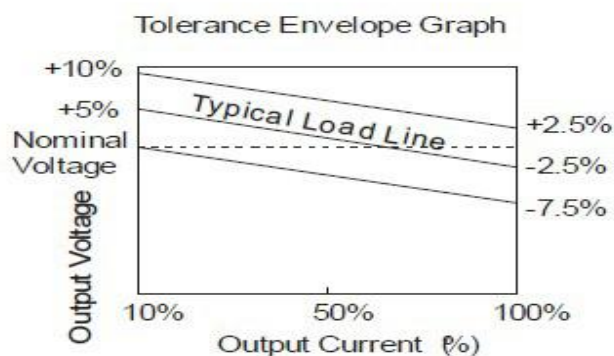
SW1-05D15		15V	3.3	33	-15V	3.3	33	
SW1-05D24		24V	2.1	21	-24V	2.1	21	
SW1-12S05S	12 V (11.4~12.6VDC)	5V	20	200				
SW1-12S09		9V	11	110				
SW1-12S12		12V	8	83				
SW1-12S15		15V	6.8	68				
SW1-12S24		24V	4.2	42				
SW1-12D05		5V	10	100	-5V	10	100	
SW1-12D09		9V	5.5	55	-9V	5.5	55	
SW1-12D12		12V	4	40	-12V	4	40	
SW1-12D15		15V	3.3	33	-15V	3.3	33	
SW1-12D24		24V	2.1	21	-24V	2.1	21	
SW1-24S05	24V (22.8~25.2VDC)	5V	20	200				
SW1-24S09		9V	11	110				
SW1-24S12		12V	8	83				
SW1-24S15S		15V	6.8	68				
SW1-24S24		24V	4.2	42				
SW1-24D05		5V	10	100	-5V	10	100	
SW1-24D09		9V	5.5	55	-9V	5.5	55	
SW1-24D12		12V	4	40	-12V	4	40	
SW1-24D15		15V	3.3	33	-15V	3.3	33	
SW1-24D24		24V	2.1	21	-24V	2.1	21	

□ Shows the nominal value of input voltage,due to space limitations ,the above list is only for some products,If other than a list of products, please contact the Company's sales department.

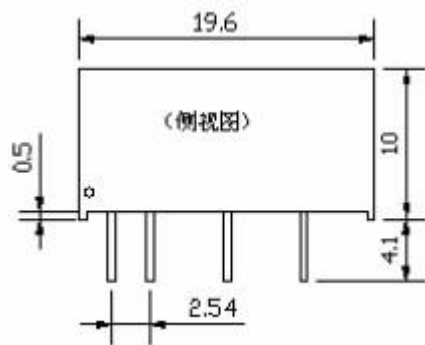
Mechanical Data

Packing Code	L x W x H : mm	Packing No.
Single O/P	19.5 x 10.00 x6	
Dual O/P	27.5X12X9.5	

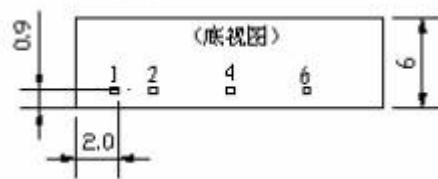
Typical Temperature Curve



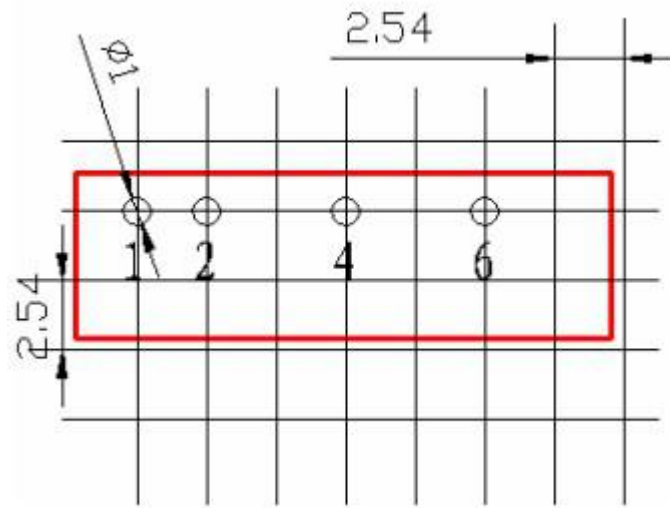
Mechanical Dimension



LATERAL VIEW



BOTTOM VIEW



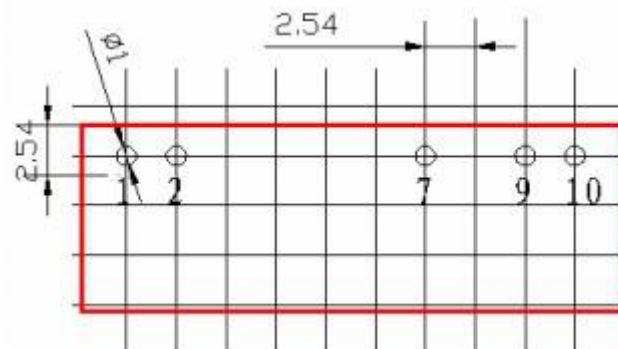
Recommended PCB Layout



LATERAL VIEW



BOTTOM VIEW



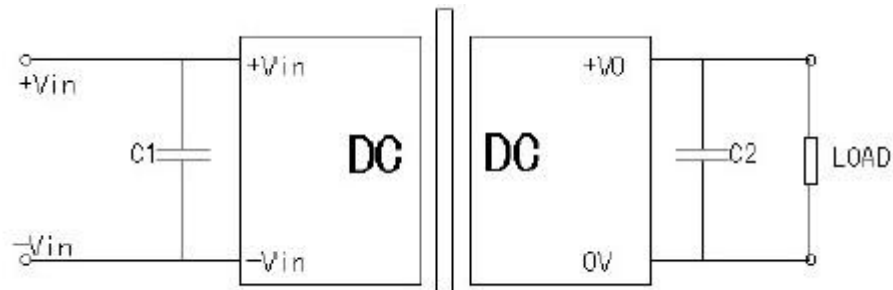
Recommended PCB Layout

UNIT:mm

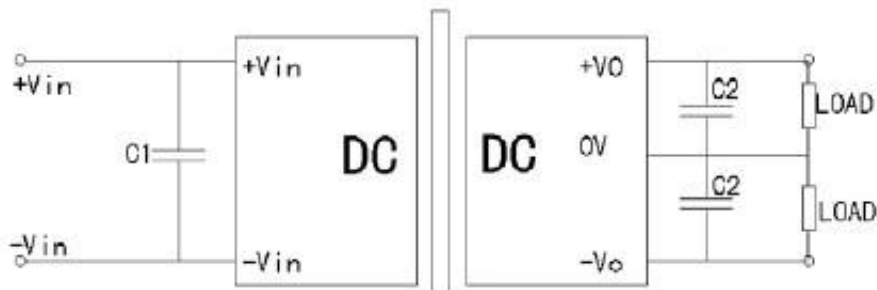
Pin Assignment

PIN	1	2	4	6						
Single output	Vin	GND	0V	+Vo						
	1	2	7	9	10					
Dual output	Vin	GND	+Vo	-Vo	0V					

Recommend Circuit



Single O/P



Dual O/P

C1、C2 select

Single O/P			
INPUT VOLTAGE	C1	OUTPUT VOLTAGE	C2
5VDC	4.7uF	5VDC	10uF
12VDC	2.2uF	9VDC	4.7uF
24VDC	1uF	12VDC	2.2uF
---	---	15VDC	1uF
Dual O/P			
5VDC	4.7uF	±5 VDC	4.7uF
12VDC	2.2uF	±9 VDC	2.2 uF
24VDC	0.47uF	±12 VDC	1 uF
---	---	±15 VDC	0.47 uF

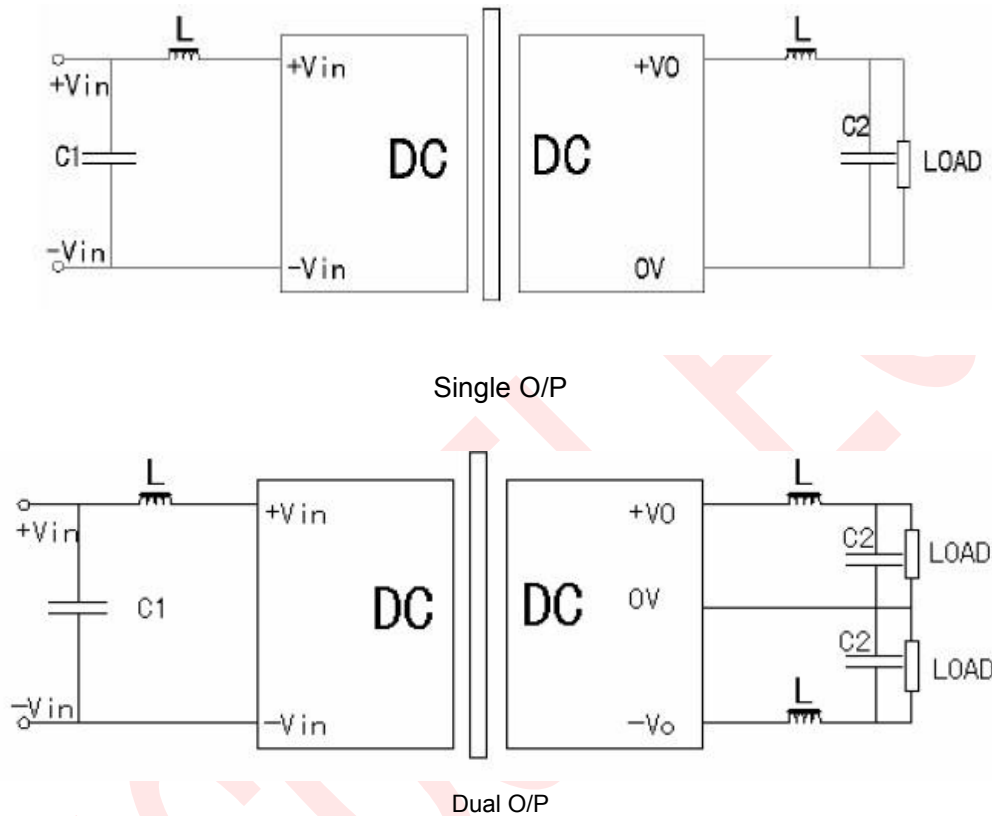
Application Note

(1)Pls don't use under no load: when the load power is less than 10% of the rated power ,we advise to connect the resistance following the output or the selection the smaller rated power module,for the resistance,the value is 5~10% of the rated power, $\text{resistance} = U_2 / (10\% \times 1W)$

(2)Pls don't connect the excessive capacitor in external circuit :output connects C2's value can't be too big,, otherwise easily lead to module startup flow or poor starting,

According to the external table to select the capacitance

(3)For the ripple&noise with higher requirements ,we advise to connect the LC filter, the frequency of LC filter is far smaller than the DC / DC module switching frequency, prevent mutual interference, resulting in increased the ripple damage the power module



*Note: The power modules such as the definition of the pin does not match with the hand book,please refer to the actual item.